



PATENT
8008-1053

THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Junichi SUZUKI et al.

Conf. 2985

Application No. 10/822,676

Group 2813

Filed April 13, 2004

METHOD OF FABRICATING SEMICONDUCTOR
MEMORY DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed Form PTO-1449.

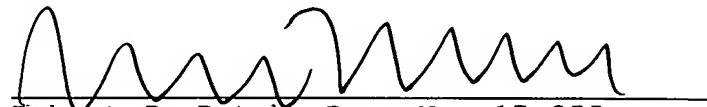
A concise explanation of the relevance of these items is that these references were cited by the Japanese Patent Office in an Official Action. A copy of the Japanese Official Action in which they were cited is attached hereto, with what is believed to be the pertinent portion enclosed in a wavy line. **An English translation of the enclosed portion is also attached hereto.** JP 2000-040754 was previously cited in the Information Disclosure Statement filed April 13, 2004.

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a

counterpart foreign application not more than three months
prior to the filing of this Statement.

Respectfully submitted,

YOUNG & THOMPSON

A handwritten signature in black ink, appearing to read 'Robert J. Patch', is written over a horizontal line.

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* Abstract provided for the Examiner's convenience